

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number : 09/418,663 Confirmation No.: 9183
Applicant : James HAKEWILL et al.
Filed : October 14, 1999
Title : METHOD AND APPARATUS FOR MANAGING THE
CONFIGURATION AND FUNCTIONALITY OF A
SEMICONDUCTOR DESIGN
TC/Art Unit : 2123
Examiner: : E. Garcia-Otero
Docket No. : 61902.000004
Customer No. : 21967

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, and in compliance with the duty of disclosure set forth in 37 C.F.R. § 1.56, applicants are submitting herewith copies of the references listed on the attached Form PTO-SB/08A for consideration and to be made of record herein by the U.S. Patent and Trademark Office in the above-captioned application.

Consideration of the foregoing plus the prompt return of a copy of the enclosed Form SB/08A with the Examiner's initials in the left column in accordance with MPEP 609 are respectfully requested.

This Supplemental Information Disclosure Statement is being submitted after the mailing of a non-final Office Action, but is believed to be prior to a final Office Action or a Notice of Allowance. Pursuant to 37 C.F.R. § 1.97(c)(2), the filing fee of \$180.00 as set forth in § 1.17(p) is included in the check submitted herewith. In the event any variance exists between the

amount enclosed and the Patent Office charges, please charge or credit any difference to the undersigned's Deposit Account No. 50-0206.

Respectfully submitted,

HUNTON & WILLIAMS LLP

Dated: April 29, 2004

By:



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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Application Number	09/418,663
Filing Date	October 14, 1999
First Named Inventor	JAMES HAKEWILL
Art Unit	2123
Examiner Name	E. Garcia-Otero
Attorney Docket Number	61902.000004

Sheet 1 of 1

U.S. PATENT DOCUMENTS

*Examiner Initials	Cite No.	DOCUMENT NUMBER Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1.				

FOREIGN PATENT DOCUMENTS

*Examiner Initial	Cite No.	FOREIGN PATENT DOCUMENT		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	TRANSLATION	
		Country Code:	Number - Kind Code (if known)				YES	NO
	2.							

NON-PATENT LITERATURE DOCUMENTS

*Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	TRANSLATION	
			YES	NO
	3.	RAINER LEUPERS and PETER MARWEDEL, Retargetable Code Generation based on Structural Processor Descriptions, Design Automation for Embedded Systems, vol. 3, no. 1, Jan 1998, p. 1-36.	<input type="checkbox"/>	<input type="checkbox"/>
	4.	GEORGE HADJIYIANNIS, SILVINA HANONO, and SCRINIVAS DEVADAS, ISDL: An Instruction Set Description Language for Retargetability, 4 pages.		
	5.	VOJIN ZAVOJNOVIC, STEFAN PEES, CHRISTIAN SCHLAEGER, MARKUS WILLEMS, RAINER SCHOENEN and HEINRICH MEYER, DSP Processor/Compiler Co-Design A Quantitative Approach, Integrated Systems for Signal Processing, Aachen University Technology, p. 761-765.		
	6.			
	7.			
	8.			
	9.			

EXAMINER SIGNATURE

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.